

METHOD AND DEVICE FOR GENERATING TRANSMISSION SIGNAL**Publication number:** JP2001308815 (A)**Publication date:** 2001-11-02**Inventor(s):** KANEKO KEIICHI; MATSUI KAZUNARI; TAKAOKA KATSUMI**Applicant(s):** VICTOR COMPANY OF JAPAN**Classification:**- international: **H04J11/00; H04J11/00;** (IPC1-7): H04J11/00

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Application number: JP20000116364 20000418**Priority number(s):** JP20000116364 20000418**Abstract of JP 2001308815 (A)**

PROBLEM TO BE SOLVED: To compensate for the deterioration of a modulation characteristic caused by the difference of operating time between a common-mode signal and an orthogonal signal of a digital orthogonal modulator. **SOLUTION:** A digital information signal to be transmitted is digitally modulated, by using an IFFT (13) that operates in a clock frequency in which the sampling clock of the digital orthogonal modulator is doubled, every other obtained discrete orthogonal frequency signal is supplied to the digital orthogonal modulator (15), while the obtained discrete orthogonal frequency signal is temporarily stored in an output buffer (14), and the phase error of a modulation signal, caused by a timing difference when the digital orthogonal modulator operates the common-mode signal and the orthogonal signal supplied from the IFFT, is suppressed.

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